

Amendments to the Abstract:

ABSTRACT OF THE DISCLOSURE

~~The~~ A semiconductor device of the invention which achieves a high-speed memory access. ~~When the semiconductor device is configured to include a microprocessor and a semiconductor memory, the~~ The microprocessor includes an input/output buffer for system side that is ~~made~~ capable of exchanging signals with the outside ~~by being~~ when supplied with a power supply voltage. The semiconductor memory includes an internal power supply circuit that takes in the power supply voltage as a reference voltage, and generates an internal power supply voltage ~~being~~ which is substantially equal to the power supply voltage; and, it also includes an input/output buffer for the memory side that is ~~made~~ capable of exchanging signals with the input/output buffer for the system side ~~by being~~ when supplied with the internal power supply voltage. This circuit configuration ~~saves the~~ eliminates the need for level shifting on the microprocessor side, ~~and realizes a~~ thereby providing for high-speed access to the semiconductor memory from the microprocessor.